



Chip for People

# 騰富科技股份有限公司

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File: JA5856X compare note(V0.1).doc

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Subject: To compare JA5856E & JA58560 & JA5856F.

Note:

- The main deference part is options as below:

- **JA5856E (OTP) & JA58560 (Mask)**

| Option Address | Function        |         | Bit7 | Bit6 | Bit5 | Bit4 | Bit3 | Bit2 | Bit1 | Bit0 |
|----------------|-----------------|---------|------|------|------|------|------|------|------|------|
| Offset 00H     | Protected       | No      | X    | X    | X    | X    | 1    | X    | X    | X    |
|                |                 | Yes     | X    | X    | X    | X    | 0    | X    | X    | X    |
|                | Watch dog timer | Enable  | X    | X    | X    | X    | X    | 1    | X    | X    |
|                |                 | Disable | X    | X    | X    | X    | X    | 0    | X    | X    |
|                | Clock type      | RC      |      |      |      |      |      |      | 1    | 1    |
|                |                 | HFXTAL  |      |      |      |      |      |      | 1    | 0    |
|                |                 | XTAL    | X    | X    | X    | X    | X    | X    | 0    | 1    |
|                |                 | LFXTAL  |      |      |      |      |      |      | 0    | 0    |

- **JA5856F (OTP)**

| ROM Address | Function     |         | Bit7  | Bit6  | Bit5  | Bit4  | Bit3  | Bit2  | Bit1  | Bit0  |
|-------------|--------------|---------|-------|-------|-------|-------|-------|-------|-------|-------|
| Offset 00H  | PB Pull High | Disable | PB7=1 | PB6=1 | PB5=1 | PB4=1 | PB3=1 | PB2=1 | PB1=1 | PB0=1 |
|             |              | Enable  | PB7=0 | PB6=0 | PB5=0 | PB4=0 | PB3=0 | PB2=0 | PB1=0 | PB0=0 |
| Offset 01H  | PB Pull Low  | Disable | PB7=1 | PB6=1 | PB5=1 | PB4=1 | PB3=1 | PB2=1 | PB1=1 | PB0=1 |
|             |              | Enable  | PB7=0 | PB6=0 | PB5=0 | PB4=0 | PB3=0 | PB2=0 | PB1=0 | PB0=0 |
| Offset 02H  | PB Wakeup    | Disable | PB7=1 | PB6=1 | PB5=1 | PB4=1 | PB3=1 | PB2=1 | PB1=1 | PB0=1 |
|             |              | Enable  | PB7=0 | PB6=0 | PB5=0 | PB4=0 | PB3=0 | PB2=0 | PB1=0 | PB0=0 |
| Offset 05H  | Protected    | No      | X     | X     | 1     | X     | X     | X     | X     | X     |
|             |              | Yes     | X     | X     | 0     | X     | X     | X     | X     | X     |
|             | LVDT         | Enable  | X     | X     | X     | 1     | X     | X     | X     | X     |
|             |              | Disable | X     | X     | X     | 0     | X     | X     | X     | X     |
|             | WDT          | Enable  | X     | X     | X     | X     | 1     | X     | X     | X     |
|             |              | Disable | X     | X     | X     | X     | 0     | X     | X     | X     |
|             | OSC Type     | RC1     |       |       |       |       |       | 1     | 1     | 1     |
|             |              | RC2     |       |       |       |       |       | 1     | 1     | 0     |
|             |              | RC3     |       |       |       |       |       | 1     | 0     | 1     |
|             |              | RC4     |       |       |       |       |       | 1     | 0     | 0     |
|             |              | LPXTAL1 | X     | X     | X     | X     | X     | 0     | 1     | 1     |
| LPXTAL2     |              |         |       |       |       |       | 0     | 1     | 0     |       |
| XTAL        |              |         |       |       |       |       | 0     | 0     | 1     |       |
| HSXTAL      |              |         |       |       |       | 0     | 0     | 0     |       |       |



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2. When totally changes JA5856E to JA5856F, need to set up for the following parameter way:

- **PB pull high** with Disable ◦
- **PB pull low** with Disable ◦
- **PB Wakeup** with Disable ◦
- **LVDT** with Disable ◦
- **OSC Type** relations

| JA5856E |   | JA5856F |
|---------|---|---------|
| RC      |   | RC1     |
| HFXTAL  | → | HSXTAL  |
| XTAL    |   | XTAL    |
| LFXTAL  |   | LPXTAL1 |

When using RC Oscillator type, it makes its operating frequency the same as the original one to need to adjust external R and C after changing.

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